

Levelized Simulation

1. Levelization
2. Cyclic circuits: Asynchronous/Synchronous
3. Timing
4. Logic Models
5. Vector Packing
6. FHDL Simulator

Delay Models

1. Zero Delay
2. Unit Delay
3. Multi-Delay
4. Nominal Delay
5. Max/Min

Event Driven Simulation

1. Single List
2. Double List