Instructor: Peter M. Maurer
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Office Hours: 8:00 - 12:00 M-F except 9:30-11:00 TR, and 10:00-11:00 MWF.


Course Topics: Pipelining Principles
               Price/performance
               Other fundamental issues
               Instruction-set design principles
               Instruction-level parallelism
               Memory hierarchy
               Multiprocessors and Multicomputers
               I/O Storage Systems
               Interconnection Networks

Exams:         Test #1: Feb. 14 - 25%
               Test #2: Mar. 28 - 25%
               Test #3: Friday, May 9, 8:00-10:00 25%
               Term Paper: 25%